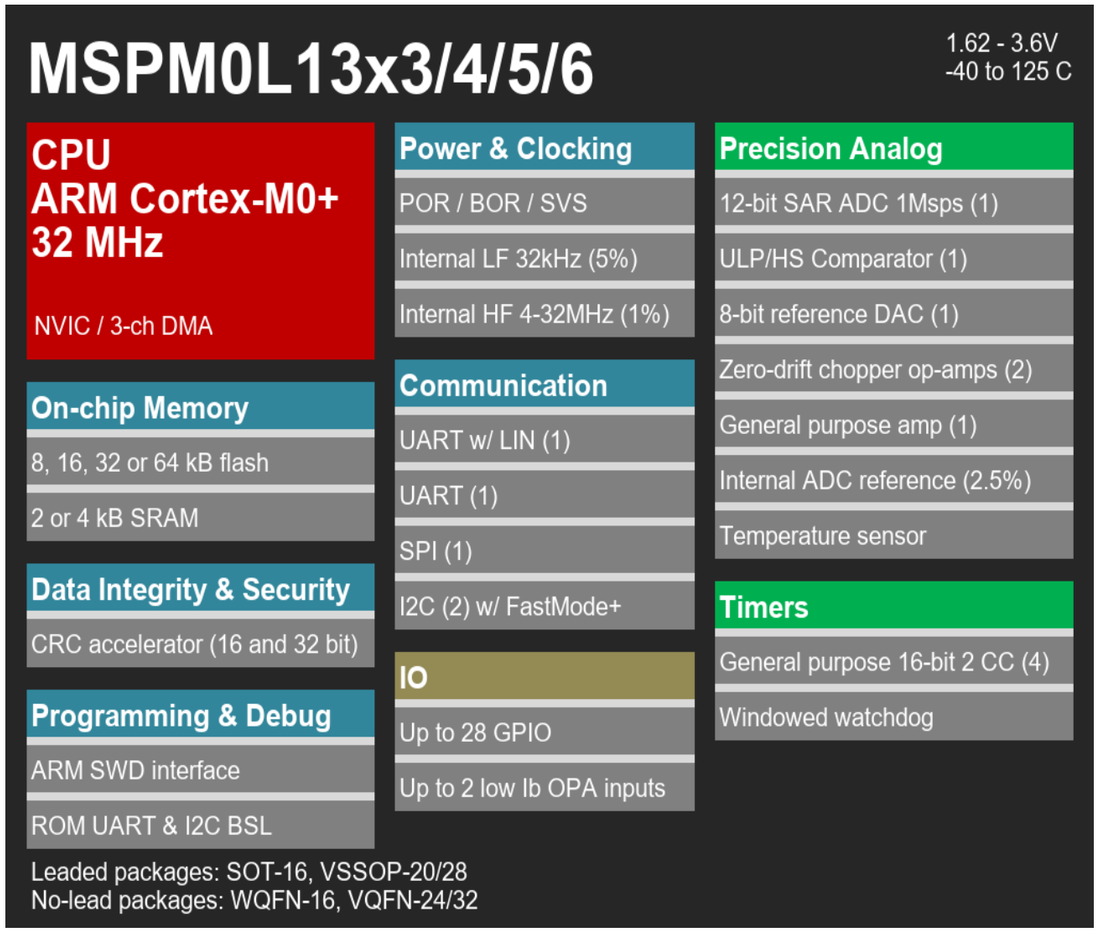
MSPM0 MCAN module introduction

—— MSPM0 peripheral training series

Presented by Yuhao Zhao

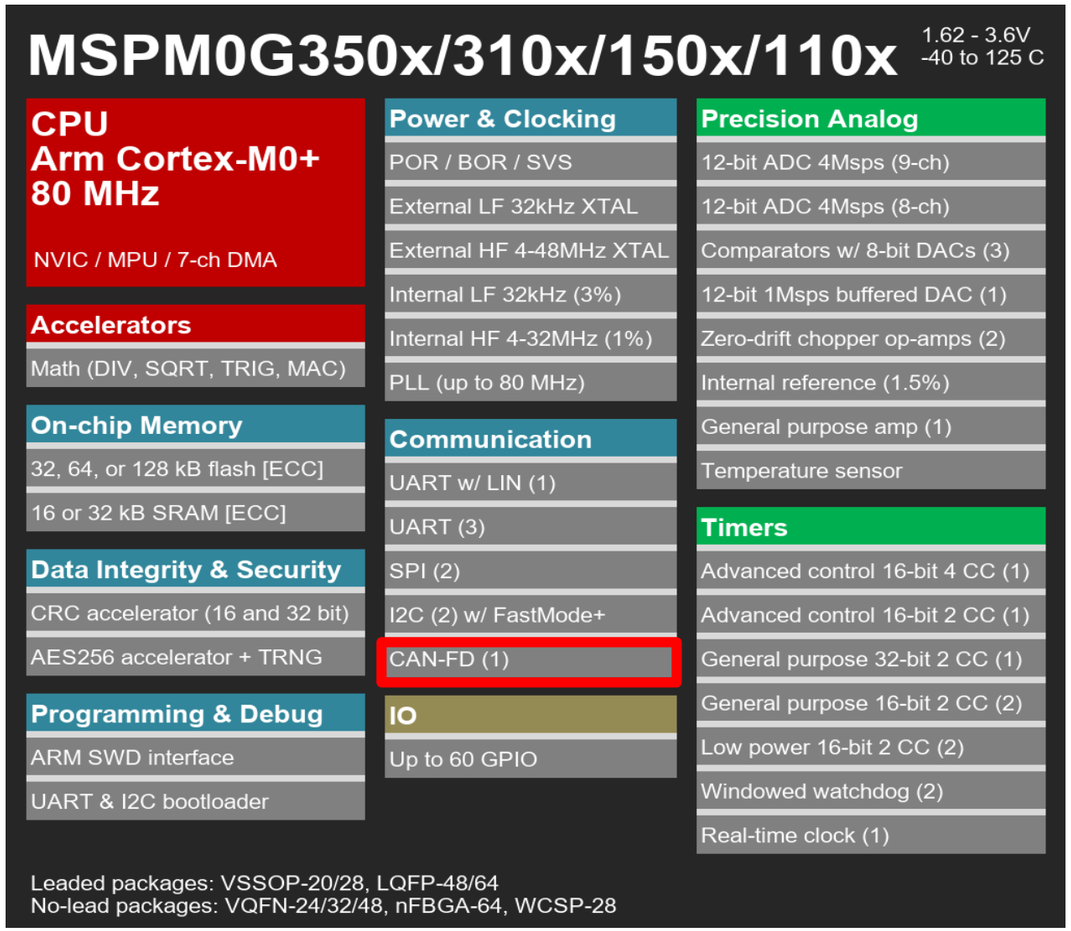
MCU level overview

——MSPM0Lxx series



32 MHz MCU with up to 64kB flash, 32 pins, 12-bit ADC, dual zero-drift OPA/PGA, COMP

——MSPM0Gxx series



80 MHz MCU with up to 128kB flash, 64 pins, advanced analog, AES/TRNG, CAN-FD

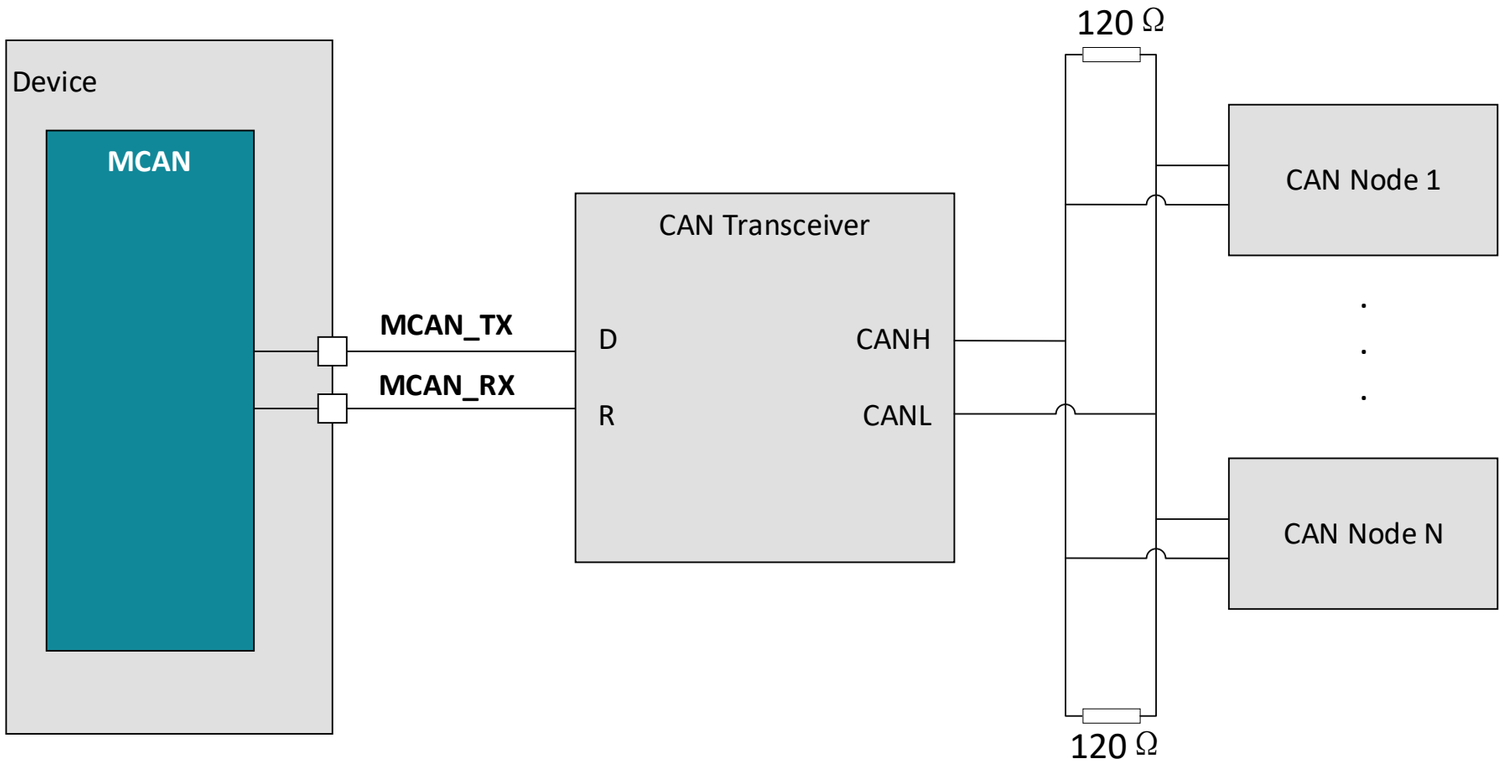
MSPM0 MCAN module introduction

Key Features

- Conforms with CAN Protocol 2.0 A, B and ISO 11898-1:2015
- Full **CAN FD** support (up to 64 data bytes and up to **5Mbit/s** rate)
- AUTOSAR and SAE J1939 support
- Clock stop and wakeup support
- Up to 128 filter elements
- ECC check for Message RAM
- Up to 32 dedicated transmit buffers
- Up to 64 dedicated receive buffers

Key Differences between G and L MCUs

- MSPM0G350x series have 1x MCAN



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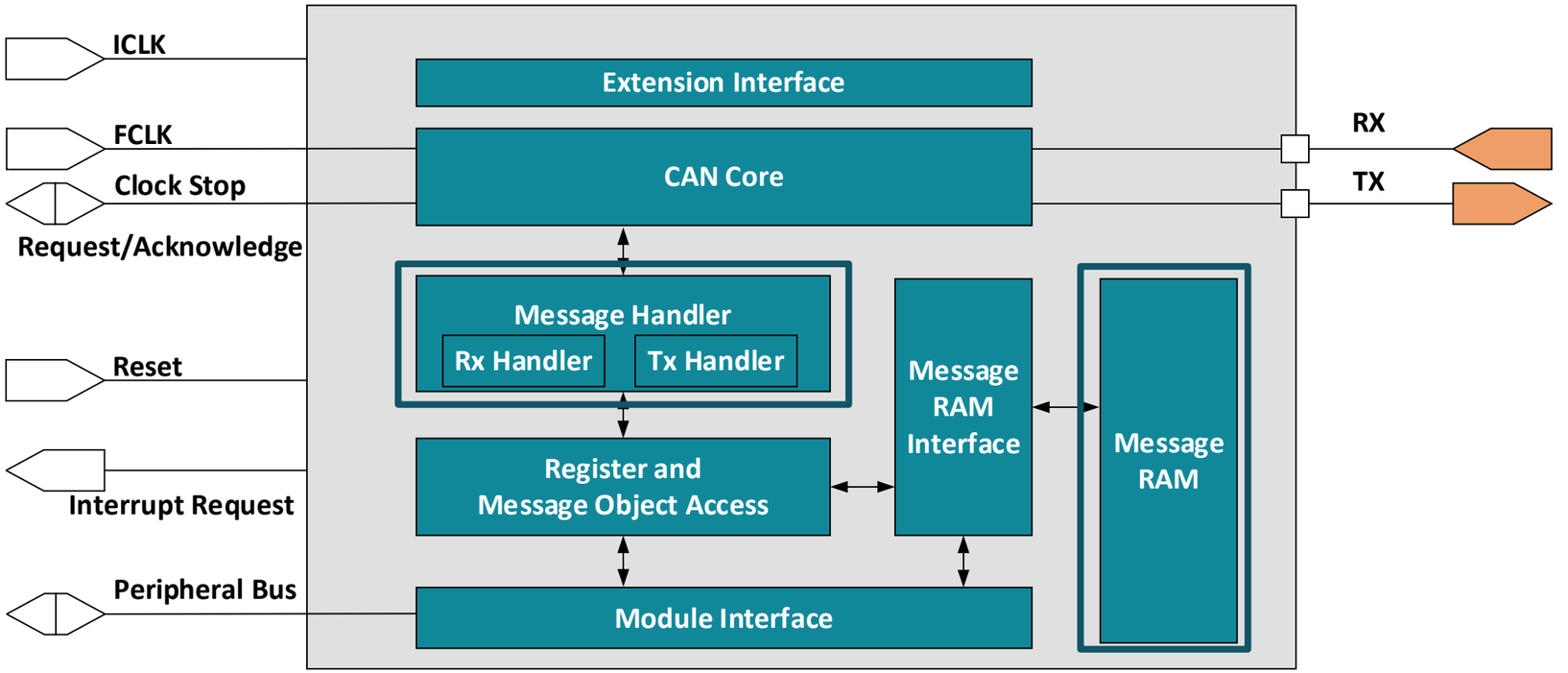


MCAN module quick start

Academy

CANFD introduction lab

Driverlib Examples

MSPM0G350x:

- mcan_loopback
- mcan_message_rx
- mcan_message_rx_tcan114x
- mcan_multi_message_tx
- mcan_multi_message_tx_tcan114x
- mcan_single_message_tx

MSPM0L13xx:

NA

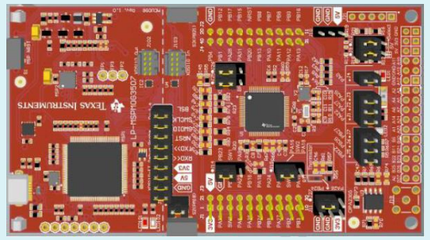
Related Links

MSPM0 online resource
MSPM0 quick start guide
MSPM0 Sysconfig user's guide

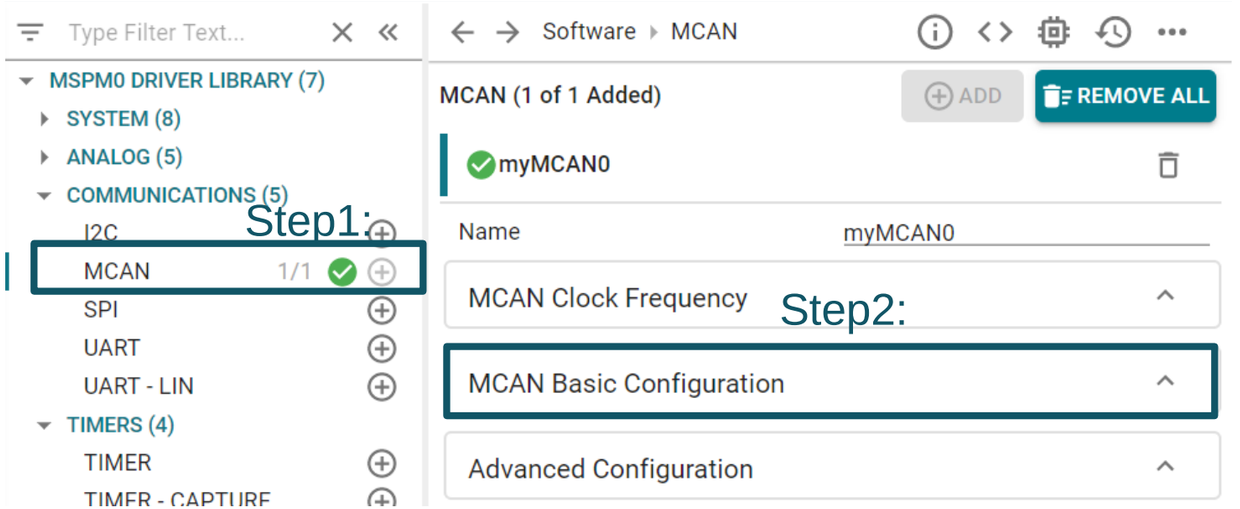
MSPM0G350x datasheet
MSPM0Gxx technical reference manual

Launchpad

LP-MSPM0G3507



Sysconfig Entrance for CAN Setting



To find more MSPM0 training series, please visit:

- TI.com.cn
- WeChat (德州仪器公众号)
- Bilibili
- 21IC